Applic. No. 10/670,664 Reply to Office Action dated 04/14/2006 Amendment dated 07/11/2006 Replacement Sheet JUL 1 7 2005 1/1 FIG.1 -PGEN Memory **Devices** 101 -102 DL2 DL1 103 BUS₁ C OCDS D 104 BUS₂ DL3 DL4 **OCDS** Module Core External Debugger Microcontroller Peripheral Devices Enhanced OCDS FIG.2 Module **OCDS OCDSN OCDS Module** External Debugger Core D CU Control CM Configuration Device Device Memory FIG.3 **PRIOR ART** - PGE External Debugger Memory OCDS Module Devices DL2 DL1 BUS1 **OCDS** Core BUS₂ DL3 DL4 Microcontroller Peripheral Units -